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<b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)		<b>Complete if Known</b>			
		Application Number	10/720,568		
		Filing Date	November 24, 2003		
		First Named Inventor	Jianhong Ju et al.		
		Group Art Unit	2836 2819		
		Examiner Name	Not yet assigned		
Sheet	1	of	1	Attorney Docket Number	112055-0074U

OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
DC	1.	TEXAS INSTRUMENTS INCORPORATED, SN65LVDS32B, SN65LVDT32B, SN65LVDS3486B, SN65LVDT3486B, SN65LVDDS9637B, SN65LVDT9637B HIGH-SPEED DIFFERENTIAL RECEIVERS, October 2000 SLLS440A Product Reference, Revised May 2001, PR1-19	
DC	2.	MAXIM, Low-Jitter, 800Mbps, 10-Port LVDS Repeaters with 100Ω Drive, Application Notes for the MAX9153/MAX9154, Revised October 2001, PR1-11	
DC	3.	GINGERICH, KEVIN, Wired-Logic Signaling With M-LVDS: High Performance Linear-Interface, Application Report SLLA119 for Texas Instruments, dated October 2002, PR1-14	

Examiner Signature	<i>Paul D. Day</i>	Date Considered	2/25/05
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